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R. Nagarajan 12

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Ramesh Nagarajan
Case: 12
Serial No.: To Be Assigned
Filing Date: June 6, 2000
Title: Efficient Architectures for Protection Against Network Failures
Group: To Be Assigned
Examiner: To Be Assigned



INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed document is enclosed.

U.S. Patents

U.S. Patent No. 6,021,113 issued on 02/01/00 to Doshi et al.

U.S. Patent No. 5,581,689 issued on 12/03/96 to Slominski et al.

U.S. Patent No. 5,537,532 issued on 07/16/96 to Chng et al.

U.S. Patent No. 5,435,003 issued on 07/18/95 to Chng et al.

U.S. Patent No. 5,093,824 issued on 03/03/92 to Coan et al.

U.S. Patent No. 4,956,835 issued on 09/11/90 to Grover

1. J. Anderson et al., "Fast Restoration of ATM Networks," IEEE Journal on Selected Areas in Communications, Vol. 12, No. 1, pp. 128-138, January 1994.

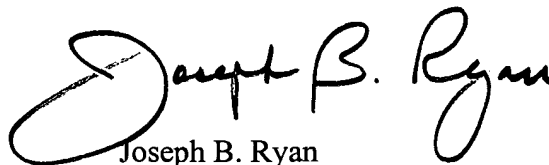
2. W.D. Grover, "The SelfHealing™ Network: A Fast Distributed Restoration Technique for Networks Using Digital Cross Connect Machines," IEEE Globecom '87, pp. 1090-1095, 1987.

3. C.H. Yang et al., "FITNESS: Failure Immunization Technology for Network Service Survivability," IEEE Globecom '88, pp. 1549-1554, 1988.

4. C.E. Chow et al., "A Fast Distributed Network Restoration Algorithm," IEEE Globecom '93, pp. 261-267, 1993.
5. S. Hasegawa et al., "Control Algorithms of SONET Integrated Self-Healing Networks," IEEE Journal on Selected Areas in Communications, Vol. 12, No. 1, pp. 110-119, January 1994.
6. W.D. Grover et al., "Near Optimal Spare Capacity Planning in a Mesh Restorable Network," IEEE Globecom '91, pp. 2007-2012, 1991.
7. H. Komine et al., "A Distributed Restoration Algorithm for Multiple-Link and Node Failures of Transport Networks, IEEE Globecom '90, pp. 459-463, 1990.
8. B.T. Doshi et al., "Dual (SONET) Ring Interworking: High Penalty Cases and How to Avoid Them," Proceedings of ITC 15, pp. 361-370, June 1997.
9. C. Buyukkoc et al., "Load Balancing on SONET Rings," Proceedings of ICT '96, Istanbul, pp. 763-766, 1996.
10. S. Cosares et al., "An Optimization Problem Related to Balancing Loads on SONET Rings," Telecommunication Systems, Vol. 3, pp. 165-181, 1994.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,



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